



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, DC 20231.

September 5, 2000

Date

L. Napoli

Lisa Napoli

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Tongbi Jiang Attorney Docket No. : 660073.774 (500182.01)
Serial No. : 09/365,356 Group Art Unit : 2813
Filed : July 30, 1999 Examiner : Nema O. Berezny
Title : METHOD AND STRUCTURE FOR MANUFACTURING IMPROVED
YIELD SEMICONDUCTOR PACKAGED DEVICES

GENERAL AUTHORIZATION UNDER 37 C.F.R. § 1.136(a)(3)

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

With respect to the above-identified application, the Assistant Commissioner is authorized to treat any concurrent or future reply requiring a petition for an extension of time under 37 C.F.R. § 1.136(a)(3) for its timely submission as incorporating a petition therefor for the appropriate length of time. The Assistant Commissioner is also authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account No. 50-1266.

Respectfully submitted,

DORSEY & WHITNEY LLP

Dale C. Barr
Dale C. Barr

Registration No. 40,498

DCB/ln

1420 Fifth Avenue, Suite 3400
Seattle, Washington 98101-4010
Tel: (206) 903-8745
Fax: (206) 903-8820

RECEIVED
SEP 13 2000
JTC 2800 MAIL ROOM



RECEIVED

SEP 21 2000

RECEIVED

SEP 13 2000

TECHNOLOGY CENTER 2800
SPECIAL PROGRAM CENTER

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, DC 20231.

September 5, 2000
Date

L. Napoli
Lisa Napoli

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Tongbi Jiang	Attorney Docket No. :	660073.774 (500182.01)
Serial No. :	09/365,356	Group Art Unit :	2813
Filed :	July 30, 1999	Examiner :	Nema O. Berezny
Title :	METHOD AND STRUCTURE FOR MANUFACTURING IMPROVED YIELD SEMICONDUCTOR PACKAGED DEVICES		

**TRANSMITTAL FOR REVOCATION
AND SUBSTITUTE POWER OF ATTORNEY**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith and attached hereto as Addendum A is a true and correct copy of the Revocation and Substitute Power of Attorney executed June 1, 2000, in the above-identified application. The above-identified application is identified on Exhibit A.

Pursuant to 37 C.F.R. § 3.73, Michael L. Lynch, duly authorized designee of Assignee, has certified that the evidentiary documents have been reviewed, specifically the Assignment to MICRON TECHNOLOGY, INC., recorded



under Reel 010159 / Frame 0110, and certified that to the best of his knowledge and belief, title remains in the name of the Assignee.

Respectfully submitted,

DORSEY & WHITNEY LLP

Dale C. Barr

Registration No. 40,498

Enclosures:

Addendum A

Exhibit A

1420 Fifth Avenue, Suite 3400
Seattle, Washington 98101-4010
(206) 903-8800 (telephone)
(206) 903-8200 (fax)

Micron TV100\500182.01\500182.01 Rev & POA trans.doc

RECEIVED
SEP 13 2009
TC 2800 MAIL ROOM